

COVER PAGE

i.MX8MP-COMPACT-CM
Variant: Full

22/03/2022

SVN Revision: 1570

I2C USAGE AND ADDRESS TABLE

NAME	PERIPHERAL	ADDRESS
I2C1 1.8V	i.MX8MM-COMPACT-MB: Camera on CSI1, 3.3V*	(0x3C<<1)+RW
	i.MX8MM-COMPACT-MB: USB3.0 Power Switch, 3.3V*	(0x050<<1)+RW
	i.MX8MM-COMPACT-MB: RTC clock, 3.3V*	(0x51<<1)+RW
	i.MX8MM-COMPACT-MB: miniPCIe Ref. Clock, 3.3V*	(0x6A<<1)+RW
	i.MX8MM-COMPACT-MB: Audio Codec, 3.3V*	(0x1A<<1)+RW
I2C2 1.8V	i.MX8MP-COMPACT-CM: PMIC control	(0x25<<1)+RW
	i.MX8MP-COMPACT-CM: Audio Codec	(0x1A<<1)+RW
I2C3 1.8V	i.MX8MM-COMPACT-MB: PCIe M.2, 3.3V*	
	i.MX8MM-COMPACT-MB: Camera on CSI2, 3.3V*	(0x3C<<1)+RW

Note: 3.3V* - through voltage translator

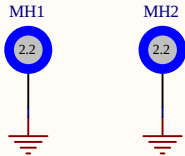
DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

Mounting Holes



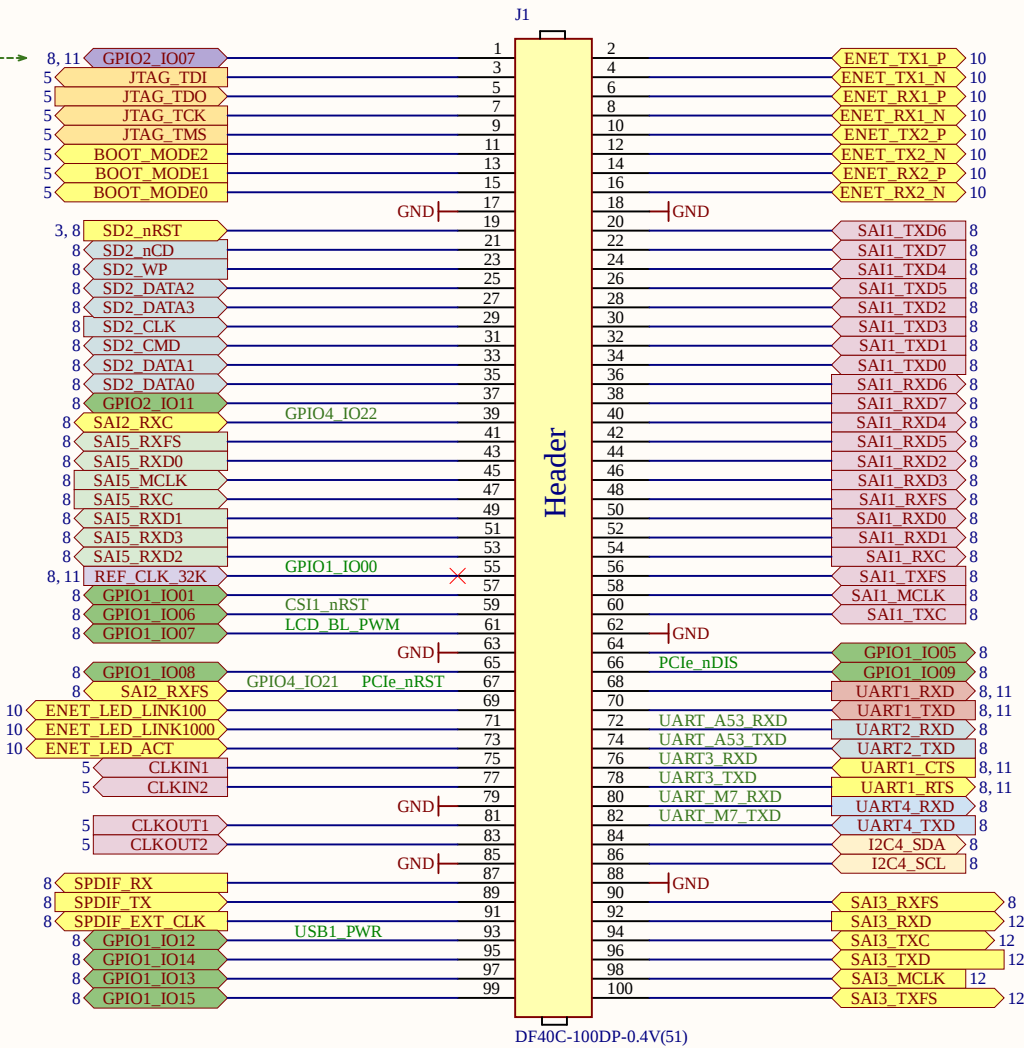
Mounting holes 5mm pad 2.2mm drill
BOARD MOUNTING HOLES - ONE IN EACH CORNER

Fiducials

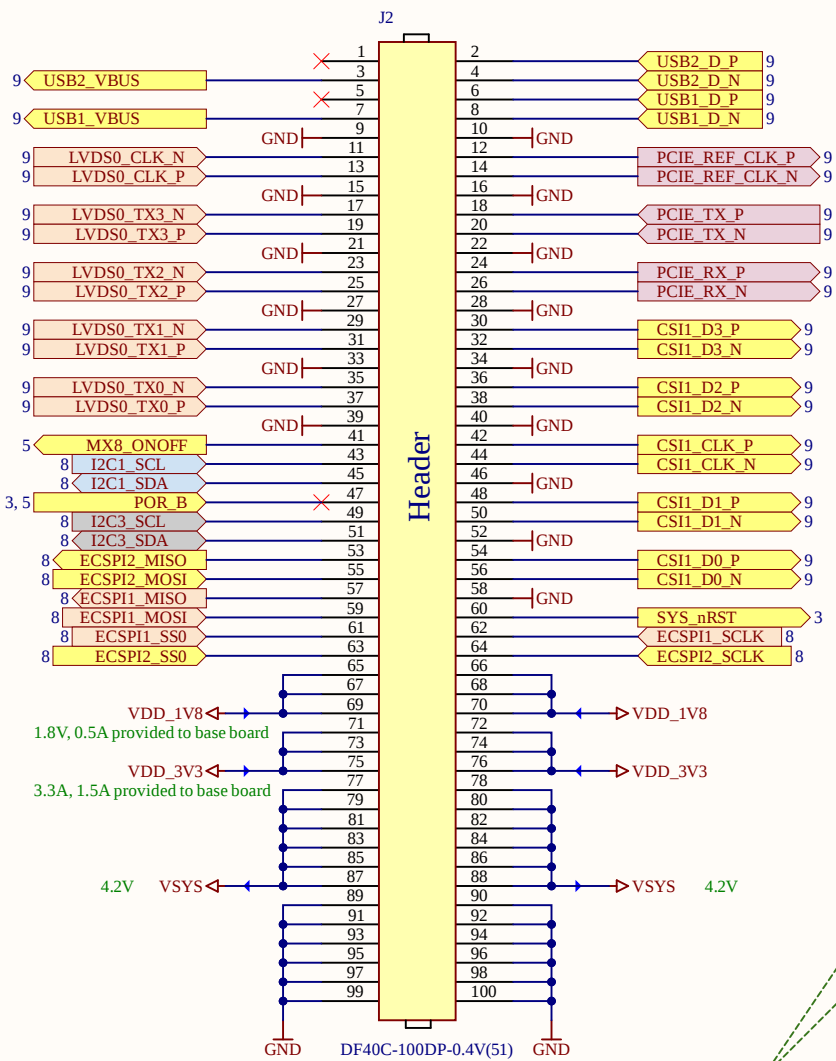


Hirose DF40C

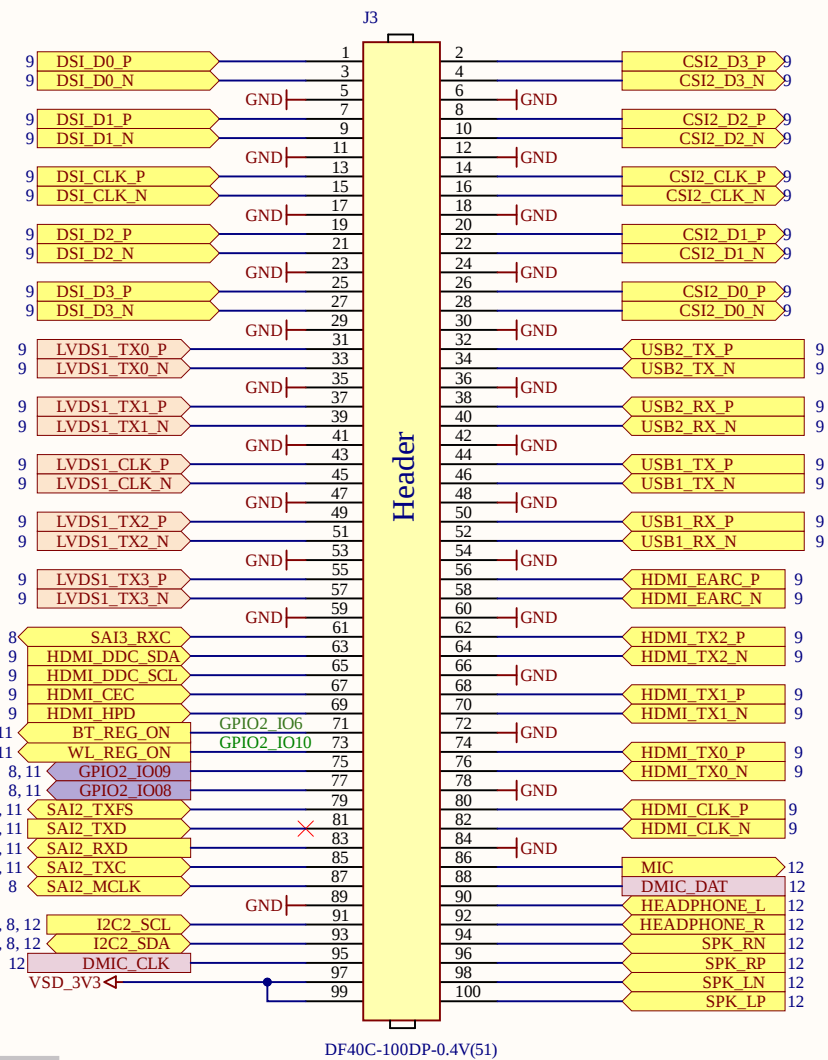
DESIGN NOTE:
Available when Bluetooth on WiFi is not used



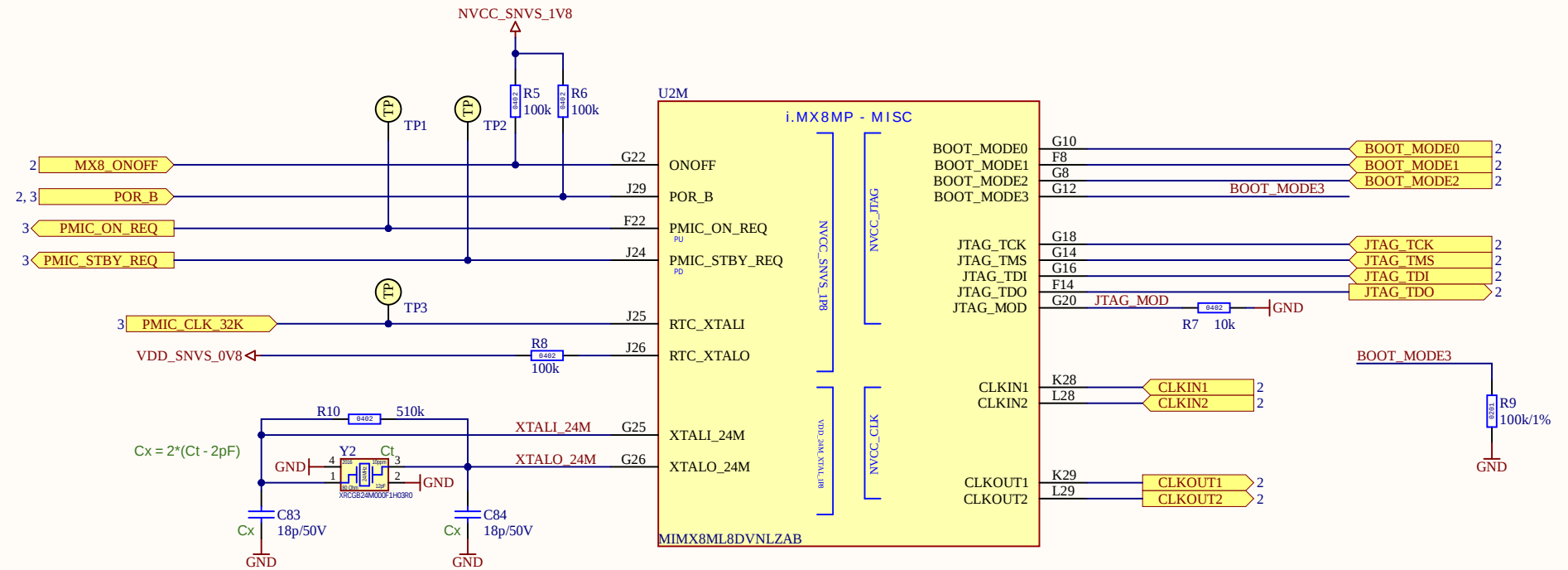
DESIGN NOTE:
UART1 - Available when
Bluetooth on WiFi is not used



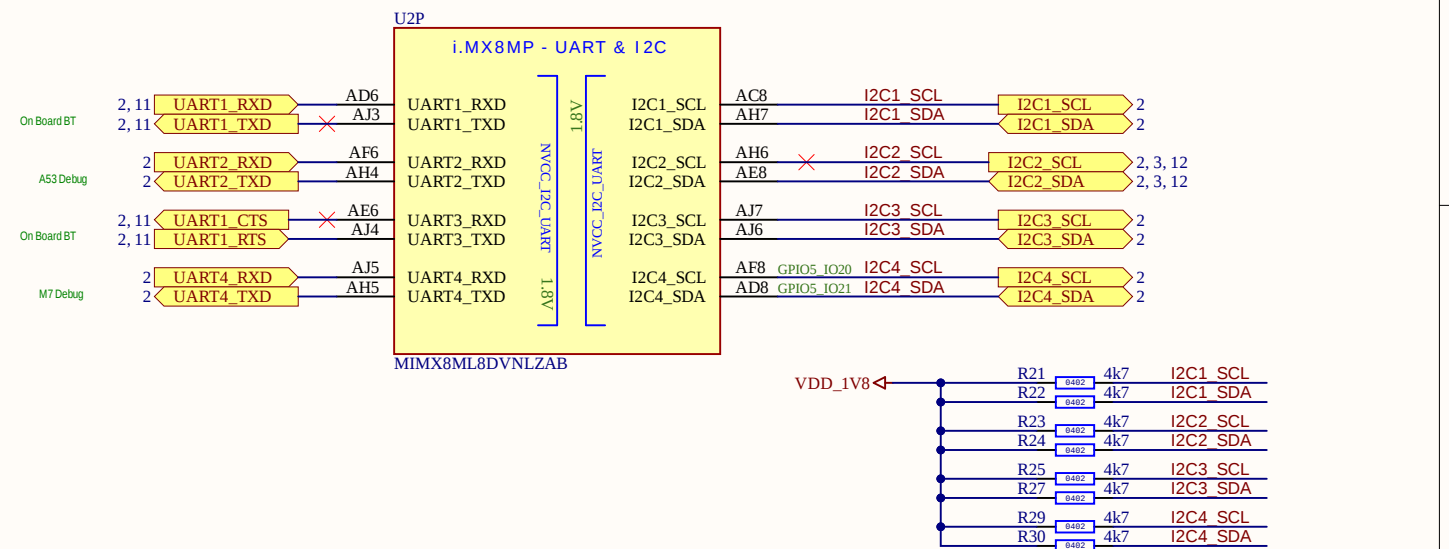
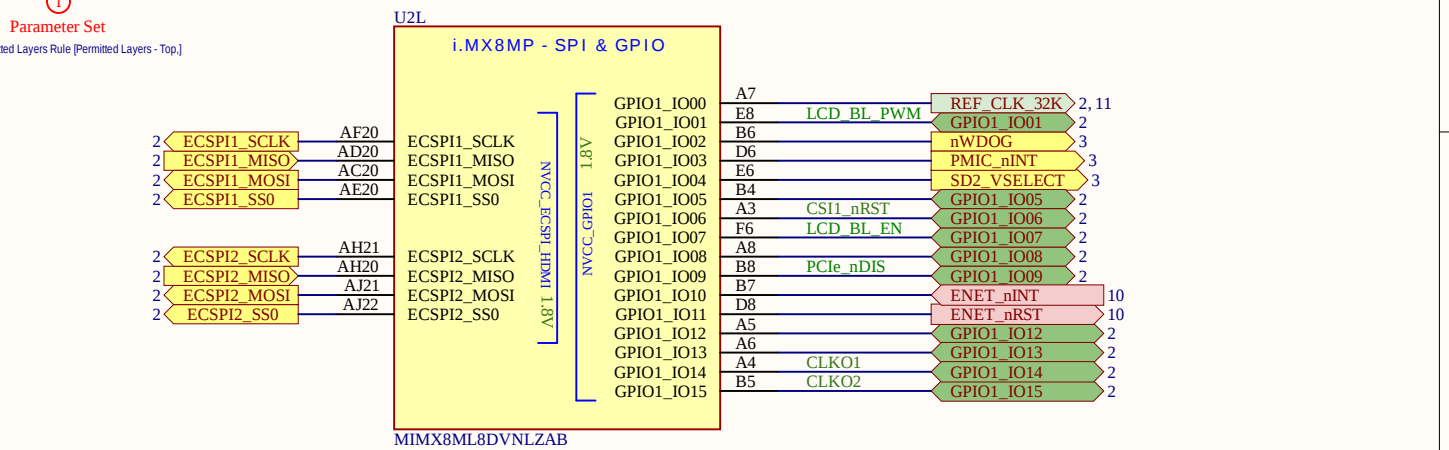
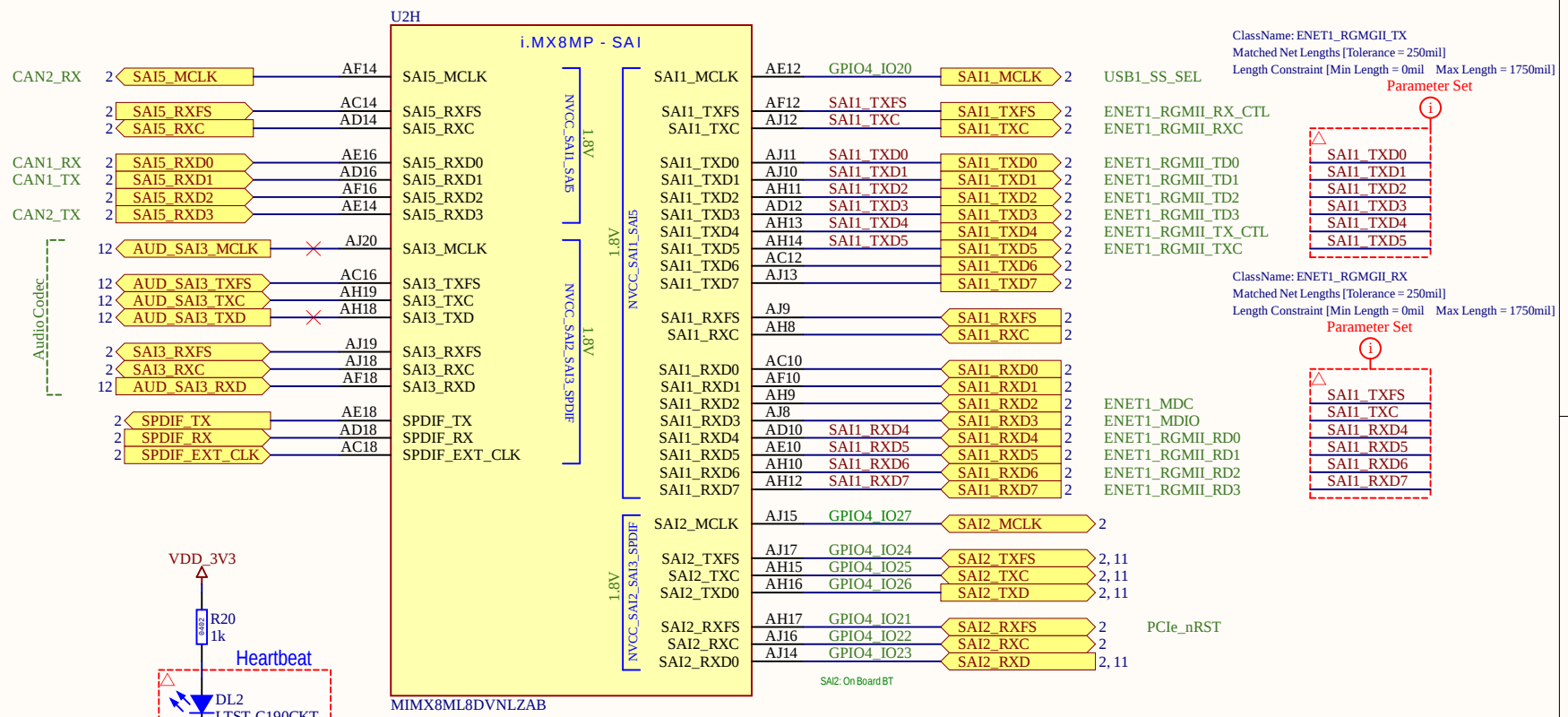
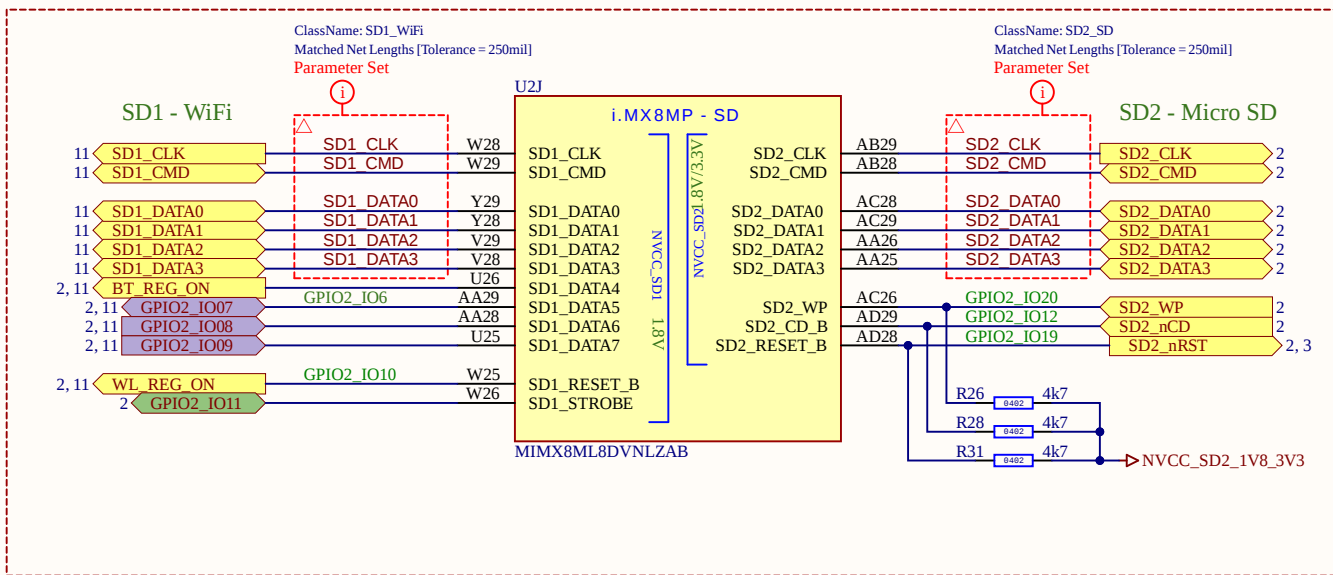
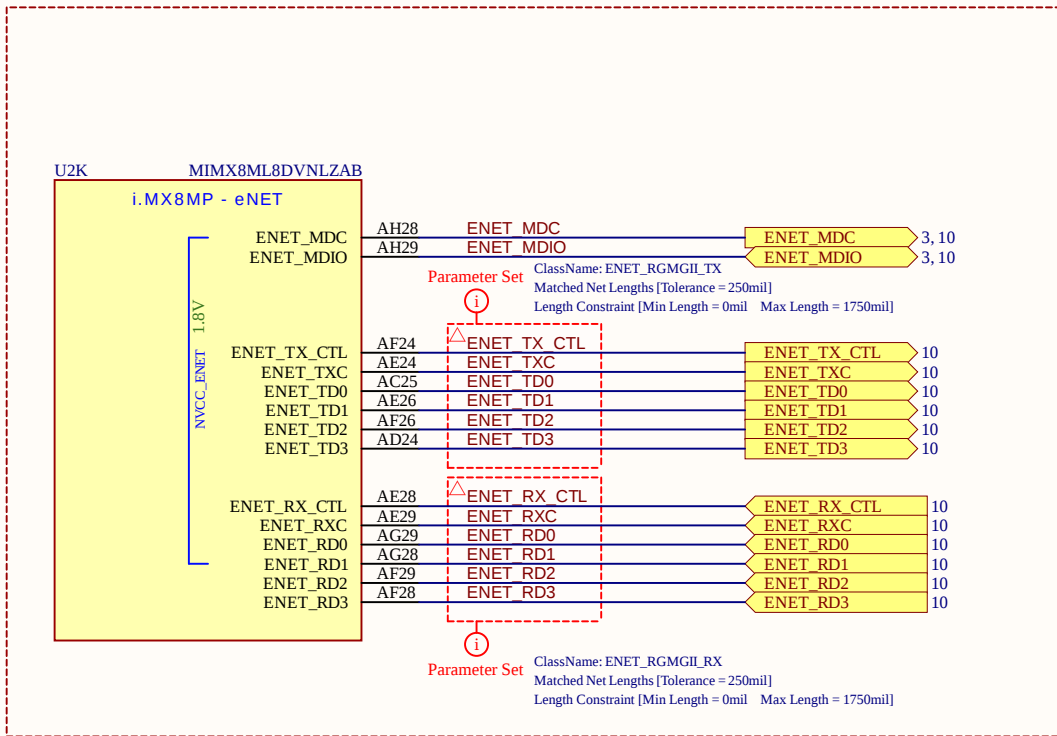
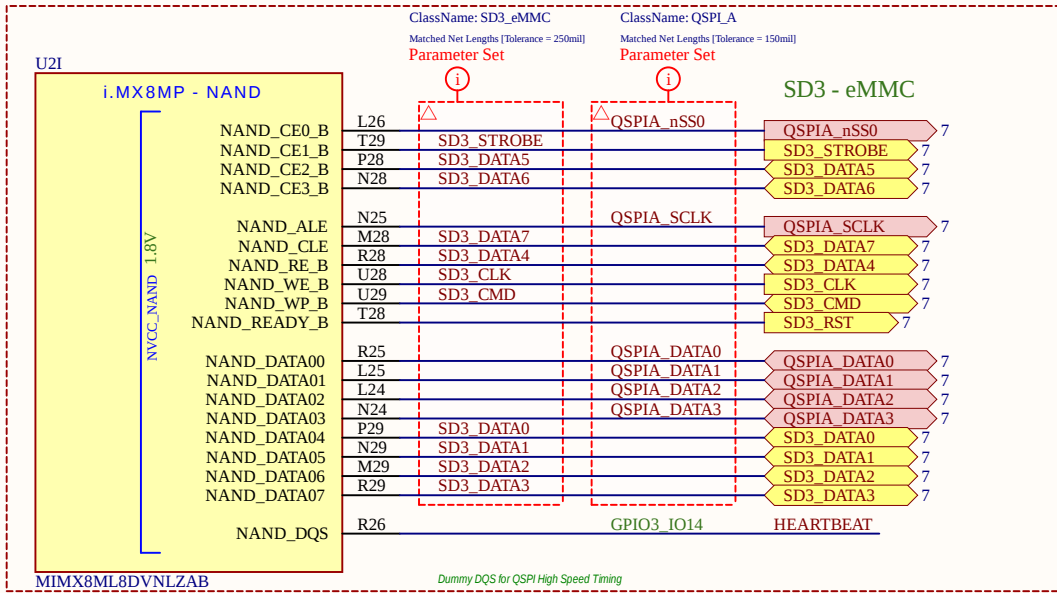
DESIGN NOTE:
Available when Bluetooth on WiFi is not used



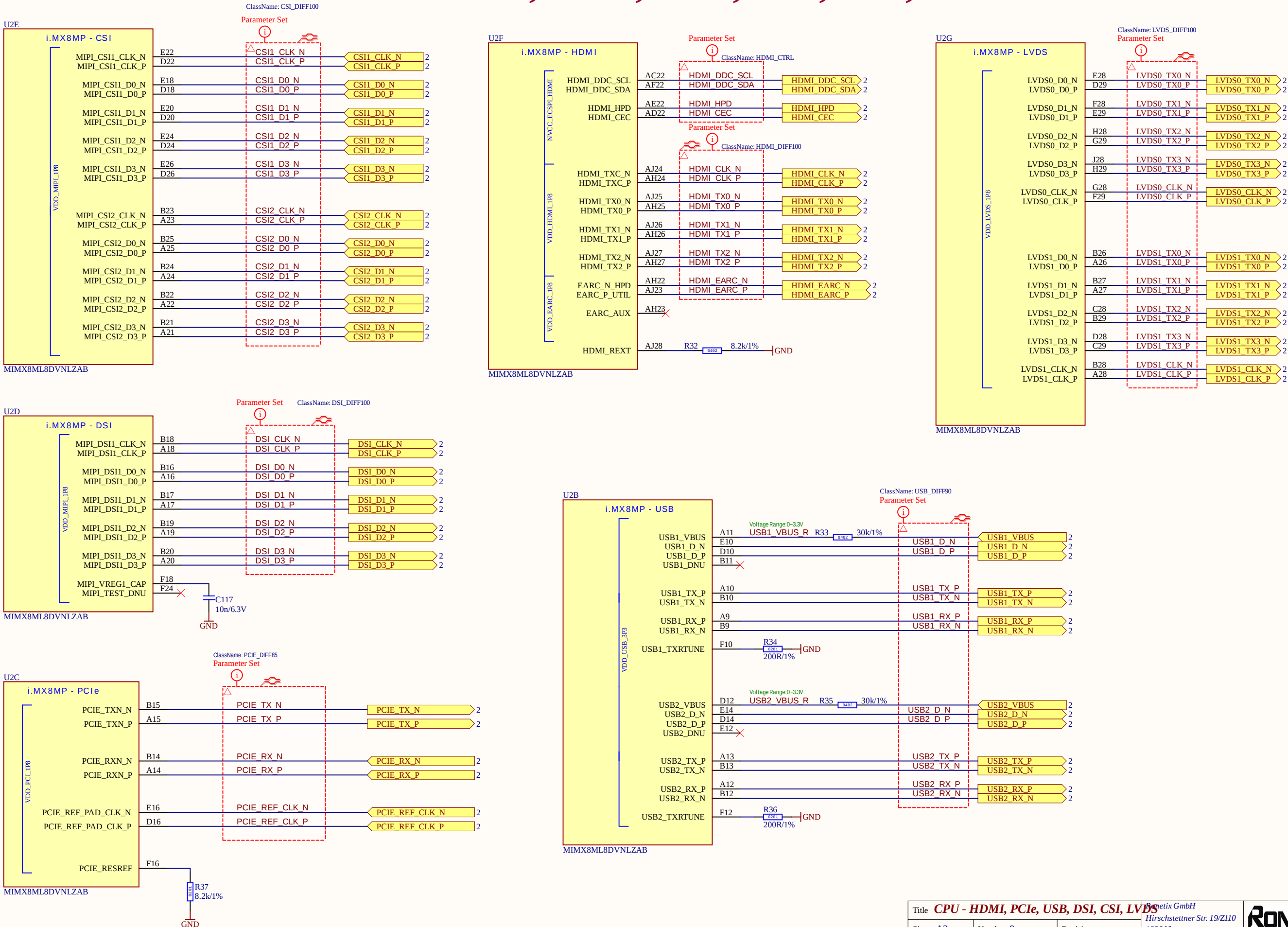
CPU Clocks, RESET



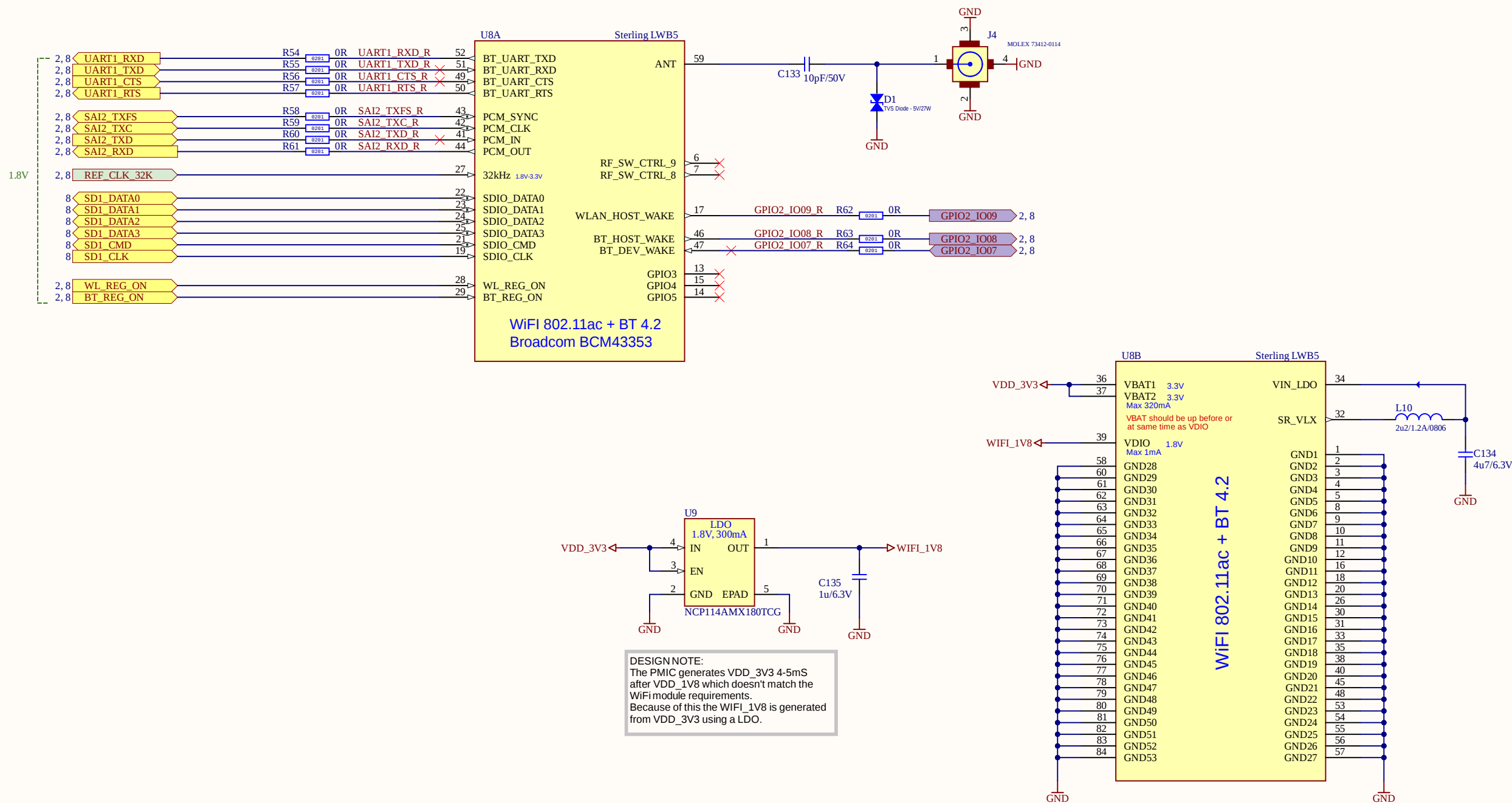
CPU - IO



CPU - HDMI, PCIe, USB, DSI, CSI, LVDS



WiFi + Bluetooth



Audio Codec

