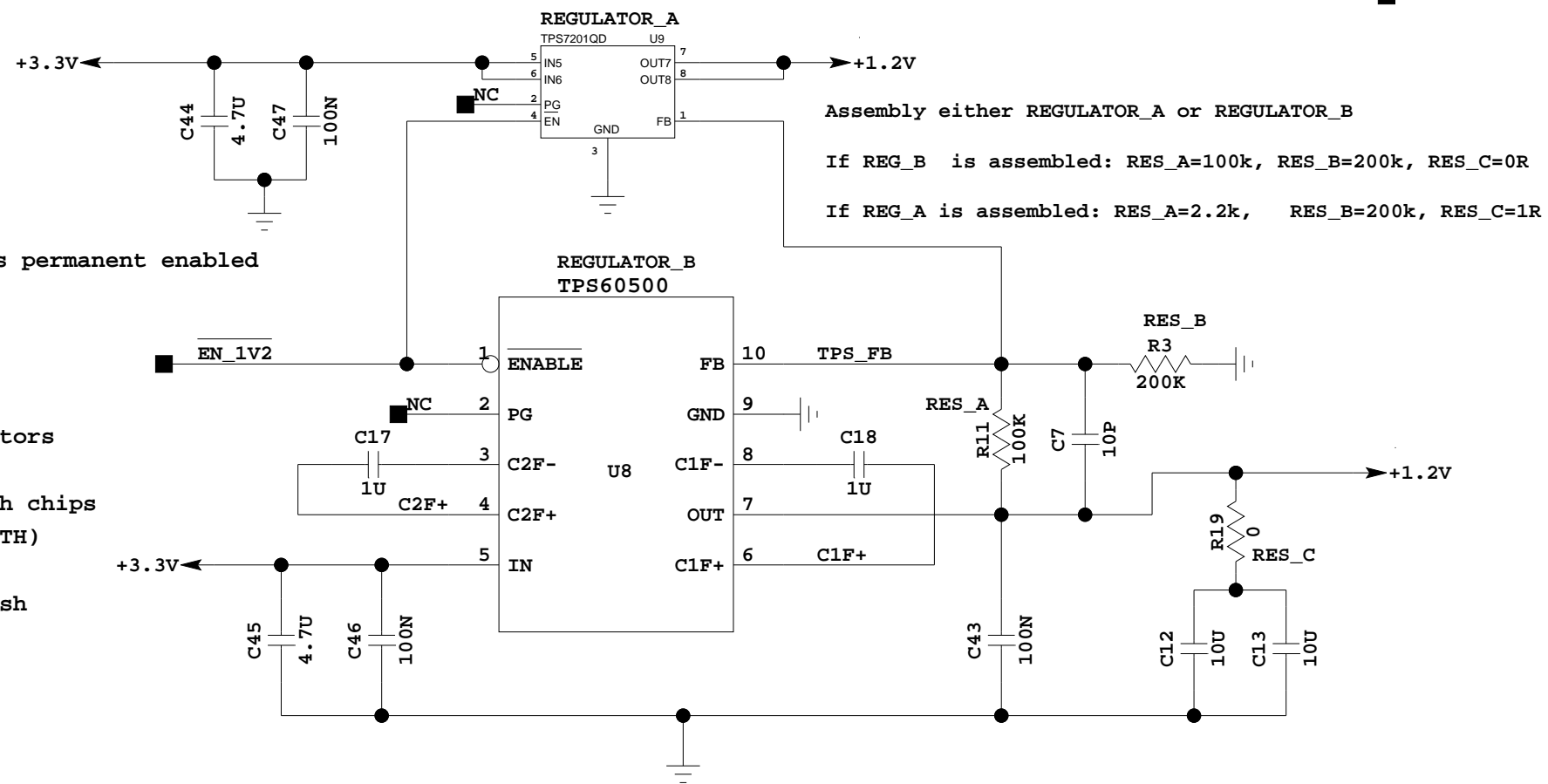


PM9263 v1.2 release notes:

- added serial 22R to SDWE, RAS, CAS, SDA10
- added pull-up/pull-down terminating for SDCK
- added R53, serial to XOUT
- add R55 - a possibility to use 50MHz for XIN
- add R56 - if not populated, then the 50MHz is permanent enabled
- populated R50 (version coding)
- changed U7 (NOR Flash): BGA64 -> BGA48

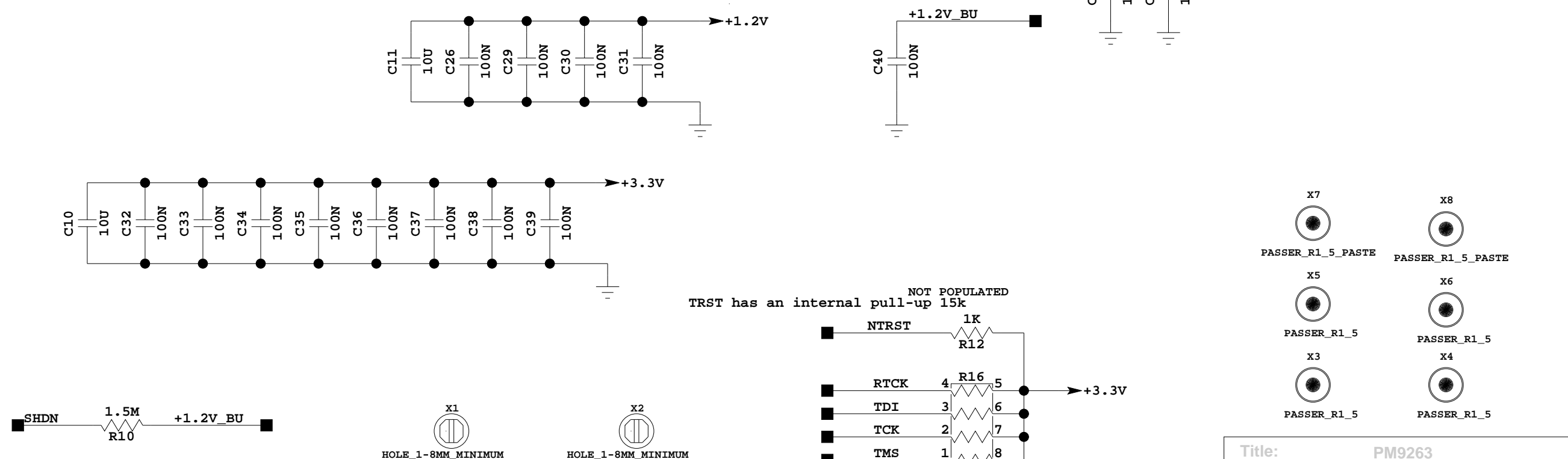
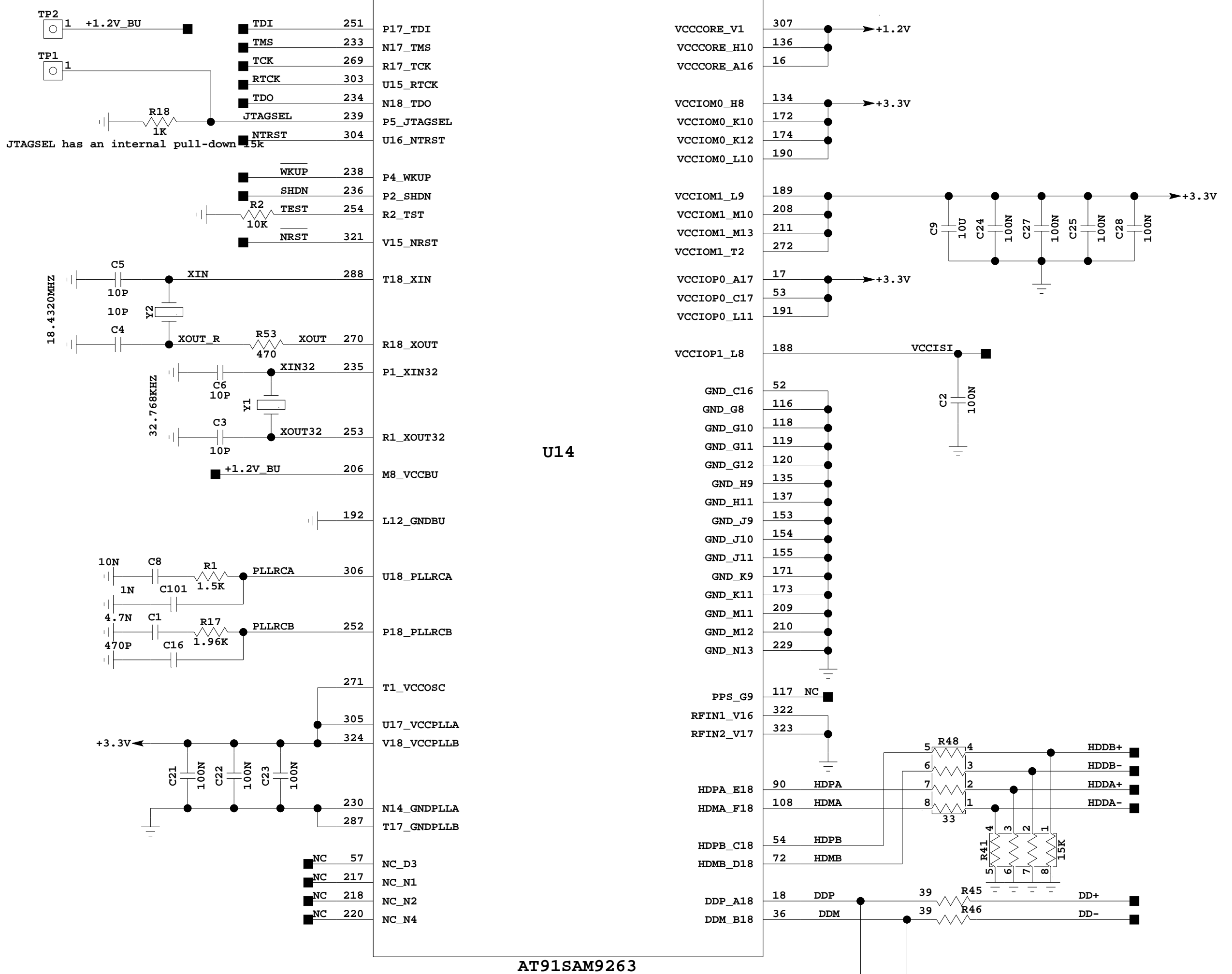
PM9263 v1.1 release notes:

- the module fits only in 2.5V SODIMM200 connectors
- PB20, PB21 can be used for version coding
- PB23, PB24, PB25 - used in 2/4 dies NAND Flash chips
- PB27 - enable/disable the 50MHz oscillator (ETH)
- removed 16-bit NAND Flash footprint
- added possibility to populate 512Mbit NOR Flash
- SODIMM200, pin 200 is not connected
- added several test points



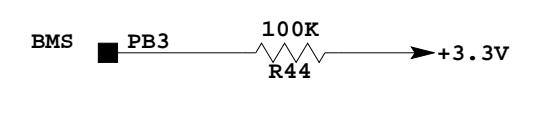
JTAG Boundary scan tester should short TP1 and TP2

Place TP1 and TP2 close to another



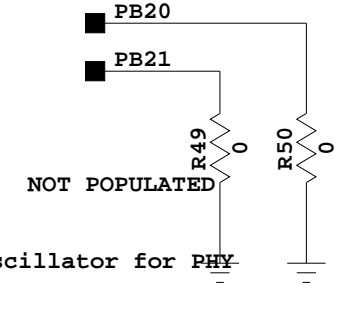
PA[0:31]		U14					
SPI0_MISO/MCIO_DA0	PA0	232	PA0/MCIO_DA0/SPI0_MISO_N16	PB0/AC9FS/TF0_K17	179	PB0	LRCLK
SPI0_MOSI/MCIO_CDA	PA1	231	PA1/MCIO_CDA/SPI0_MOSI_N15	PB1/AC97CK/TK0_K16	178	PB1	BCLK
SPI0_SPCK	PA2	249	PA2/SPI0_SPCK_P15	PB2/AC97TX/TDO_K18	180	PB2	I2SDIN
MCIO_DA1	PA3	248	PA3/MCIO_DA1/SPI0_NPCS1_P14	BMS_PB3/AC97RX/RD0_K13	175	PB3	BMS
MCIO_DA2	PA4	213	PA4/MCIO_DA2/SPI0_NPCS2_M15	PB4/TWD/RK0_L14	194	PB4	ISI_SDA
SPI0_NPCS0/MCIO_DA3	PA5	215	PA5/MCIO_DA3/SPI0_NPCS0_M17	PB5/TWCK/RFO_J15	159	PB5	ISI_SCL
MCII1_CK	PA6	212	PA6/MCII1_CK/PCK2_M14	PB6/TF1/DMARQ1_J16	160	PB6	CF_INPACK_N/DMARQ
MCII1_CDA	PA7	214	PA7/MCII1_CDA_M16	PB7/TK1/PWM0_J17	161	PB7	B_LED1
MCII1_DA0	PA8	216	PA8/MCII1_DA0_M18	PB8/TD1/PWM1_J18	162	PB8	B_LED2
MCII1_DA1	PA9	195	PA9/MCII1_DA1_L15	PB9/RD1/LCDDC_J13	157	PB9	VCTRL
MCII1_DA2	PA10	197	PA10/MCII1_DA2_L17	PB10/RK1/PCK1_J14	158	PB10	CF_BVD2
MCII1_DA3	PA11	198	PA11/MCII1_DA3_L18	PB11/RF1/SPI0_NPCS3_J12	156	PB11	SPI0_NPCS3
MCIO_CK	PA12	196	PA12/MCIO_CK_L16	PB12/SPI1_MISO_H18	144	PB12	SPI1_MISO
CANTX	PA13	193	PA13/CANTX/PCK0_L13	PB13/SPI1_MOSI_H15	141	PB13	SPI1_MOSI
CANRX	PA14	176	PA14/CANRX/IRQ0_K14	PB14/SPI1_SPCK_H17	143	PB14	SPI1_SPCK
IRQ1	PA15	177	PA15/TCLK2/IRQ1_K15	PB15/SPI1_NPCS0_H14	140	PB15	SPI1_NPCS0
SD/MMC_WP	PA16	237	PA16/MCIO_CDB/EBI1_D16_P3	PB16/SPI1_NPCS1/PCK1_B17	35	NC	
MCII1_WP	PA17	289	PA17/MCIO_DB0/EBI1_D17_U1	PB17/SPI1_NPCS2/TIOA2_H13	139	NC	
CANRX_EN	PA18	255	PA18/MCIO_DB1/EBI1_D18_R3	PB18/SPI1_NPCS3/TIOB2_G18	126	NC	
CANRS	PA19	273	PA19/MCIO_DB2/EBI1_D19_T3	PB19/LVRST_H12	138	NC	
MCII1_CD	PA20	290	PA20/MCIO_DB3/EBI1_D20_U2	PB20/CKSYNC_G14	122	PB20	
SD/MMC_CD	PA21	274	PA21/MCII1_CDB/EBI1_D21_T4	PB21/PCTL0_G13	121	PB21	
FW_CTRL	PA22	308	PA22/MCII1_DB0/EBI1_D22_V2	PB22/CKDAT_G17	125	PB22	NAND_CS2
CF/IDE_RESET	PA23	291	PA23/MCII1_DB1/EBI1_D23_U3	PB23/GPSSYNC_G15	123	PB23	NAND_CS3
CF_READY/INT	PA24	292	PA24/MCII1_DB2/EBI1_D24_U4	PB24/OTG_SE0_VM/DMARQ3_H16	142	PB24	NAND_CS4
USB_CONNECT	PA25	256	PA25/MCII1_DB3/EBI1_D25_R4	PB25/OTG_DAT_VF_F15	105	NC	
RS232_TXD0	PA26	275	PA26/TXD0/EBI1_D26_T5	PB26/OTGTP_OE_F14	104	NC	
RS232_RXD0	PA27	309	PA27/RXD0/EBI1_D27_V3	PB27/OTGRCV/PWM2_F17	107	EN_OSC	Enable 50MHz oscillator for PHY 1 - enable 0 - disable
RS232_RTS0	PA28	293	PA28/RTS0/EBI1_D28_U5	PB28/OTGSUSPEND/TCLK0_G16	124		
RS232_CTS0	PA29	310	PA29/CTS0/EBI1_D29_V4	PB29/OTGINT/PWM3_F16	106	PB29	CRE
CF_CD1	PA30	257	PA30/SCK0/EBI1_D30_R5	PB30/OTGTWD_E15	87	PB30	NAND_RD/BY
SPI1_BUSY	PA31	276	PA31/DMARQ0/EBI1_D31_T6	PB31/OTGTWCK_E17	89	PB31	IDNR
VSYNC	PC0	88	PC0/LCDVSYNC_E16	PD0/TXD1/SPI0_NPCS2_B10	28	PD0	RS232_TXD1
HSYNC	PC1	71	PC1/LCDHSYNC_D17	PD1/RXD1/SPI0_NPCS3_D10	64	PD1	RS232_RXD1
DCLK	PC2	70	PC2/LCDDOTCLK_D16	PD2/TXD2/SPI1_NPCS2_C10	46	PD2	SPI1_NPCS2
DTMG	PC3	51	PC3/LCDDEN/PWM1_C15	PD3/RXD2/SPI1_NPCS3_F10	100	PD3	SPI1_NPCS3
BUT1	PC4	86	PC4/LCDD0/LCDD3_E14	PD4/FIQ/DMARQ2_A10	10	PD4	FIQ
BUT2	PC5	34	PC5/LCDD1/LCDD4_B16	PD5/EBI0_NWAIT/RTS2_G5	113	PD5	CF_NWAIT
R0	PC6	67	PC6/LCDD2/LCDD5_D13	PD6/EBI0_NCS4/CFCS0/CTS2_G3	111	PD6	CF_CS0
R1	PC7	33	PC7/LCDD3/LCDD6_B15	PD7/EBI0_NCS5/CFCS1/RTS1_F1	91	PD7	CF_CS1
R2	PC8	50	PC8/LCDD4/LCDD7_C14	PD8/EBI0_CFCE1/CTS1_G6	114	PD8	CF_CE1
R3	PC9	32	PC9/LCDD5/LCDD10_B14	PD9/EBI0_CFCE2/SCK2_H4	130	PD9	CF_CE2
R4	PC10	69	PC10/LCDD6/LCDD11_D15	PD10/SCK1_G7	115	PD10	CF_BVD1
R5	PC11	85	PC11/LCDD7/LCDD12_E13	PD11/EBI0_NCS2/TSYNC_H5	131	NC	
G3	PC12	15	PC12/LCDD8/LCDD13_A15	PD12/EBI0_A23/TCLK_G2	110	PD12	A23
G0	PC13	103	PC13/LCDD9/LCDD14_F13	PD13/EBI0_A24/TPS0_H2	128	PD13	A24
G1	PC14	49	PC14/LCDD10/LCDD15_C13	PD14/EBI0_A25_CFRNW/TPS1_H6	132	PD14	A25/CF_RNW
G2	PC15	84	PC15/LCDD11/LCDD19_E12	PD15/EBI0_NCS3/NANDCS/TPS2_H3	129	PD15	NANDCS
B3	PC16	68	PC16/LCDD12/LCDD20_D14	PD16/EBI0_D16/TPK0_H7	133	D16	D[16:31]
B4	PC17	31	PC17/LCDD13/LCDD21_B13	PD17/EBI0_D17/TPK1_G1	109	D17	
G4	PC18	102	PC18/LCDD14/LCDD22_F12	PD18/EBI0_D18/TPK2_J5	149	D18	
G5	PC19	14	PC19/LCDD15/LCDD23_A14	PD19/EBI0_D19/TPK3_J4	148	D19	
B0	PC20	66	PC20/LCDD16/E_TX2_D12	PD20/EBI0_D20/TPK4_J8	152	D20	
B1	PC21	30	PC21/LCDD17/E_TX3_B12	PD21/EBI0_D21/TPK5_J7	151	D21	
B2	PC22	83	PC22/LCDD18/E_RX2_E11	PD22/EBI0_D22/TPK6_J3	147	D22	
E_RXDV	PC23	48	PC23/LCDD19/E_RX3_C12	PD23/EBI0_D23/TPK7_J5	150	D23	
B4	PC24	13	PC24/LCDD20/E_TXER_A13	PD24/EBI0_D24/TPK8_H1	127	D24	
B5	PC25	65	PC25/LCDD21/E_RXDV_D11	PD25/EBI0_D25/TPK9_K8	170	D25	
MCLK	PC26	12	PC26/LCDD22/E_COL_A12	PD26/EBI0_D26/TPK10_J2	146	D26	
RS-DEBUG_RXD	PC27	101	PC27/LCDD23/E_RXCK_F11	PD27/EBI0_D27/TPK11_K5	167	D27	
RS-DEBUG_TXD	PC28	29	PC28/PWM0/TCLK1_B11	PD28/EBI0_D28/TPK12_K2	164	D28	
ISI_D0	PC29	47	PC29/PCK0/PWM2_C11	PD29/EBI0_D29/TPK13_K7	169	D29	
ISI_D1	PC30	11	PC30/DRXD_A11	PD30/EBI0_D30/TPK14_J1	145	D30	
ISI_D2	PC31	82	PC31/DTXD_E10	PD31/EBI0_D31/TPK15_K6	168	D31	
ISI_D3	PE0	165	PE0/ISI_D0/DRFIN0_K3	PE16/KBDPWR_N6	222	PE16	ISI_VCTRL1
ISI_D4	PE1	166	PE1/ISI_D1/DRFIN1_K4	PE17/KBDC0_N5	221	PE17	ISI_VCTRL2
ISI_D5	PE2	186	PE2/ISI_D2/DRFIN2_L6	PE18/KBDC1/TIOA0_M1	199	NC	
ISI_D6	PE3	187	PE3/ISI_D3/DRFIN3_L7	PE19/KBDC2/TIOB0_N3	219	NC	
ISI_D7	PE4	182	PE4/ISI_D4/DRFIN4_L2	PE20/KBDC3/EBI1_NWAIT_P6	240	NC	
ISI_D8	PE5	185	PE5/ISI_D5/DRFIN5_L5	PE21/E_TXCK/EBI1_NANDWE_V13	319	PE21	E_TXCK
ISI_D9	PE6	163	PE6/ISI_D6/DRFIN6_K1	PE22/E_CRS/EBI1_NCS2/NANDCS_U13	301	PE22	NCS2
ISI_D10	PE7	184	PE7/ISI_D7/DRFIN7_L4	PE23/E_TX0/EBI1_NANDOE_T13	283	PE23	E_TX0
ISI_D11	PE8	205	PE8/ISI_PCK/TIOA1_MPE24/E_TX1/EBI1_NWR3/NBS3/CFIOW_V14	PE24/E_TX1	320	PE24	E_TX1
ISI_D12	PE9	183	PE9/ISI_HSYNC/TIOB1_L3	PE25/E_RX0/EBI1_NCS1/SDCS_T14	284	PE25	E_RX0
ISI_D13	PE10	181	PE10/ISI_VSYNC/PWM3_L1	PE26/E_RX1_R14	266	PE26	E_RX1
ISI_D14	PE11	202	PE11/ISI_MCK/PCK3_M4	PE27/E_RXER/EBI1_SDCKE_U14	302	PE27	E_RXER
ISI_D15	PE12	204	PE12/KBDR0/ISI_D8_M6	PE28/E_TXEN/EBI1_RAS_P16	250	PE28	E_TXEN
ISI_D16	PE13	203	PE13/KBDR1/ISI_D9_M5	PE29/E_MDC/EBI1_CAS_T15	285	PE29	E_MDC
ISI_D17	PE14	200	PE14/KBDR2/ISI_D10_M2	PE30/E_MDIO/EBI1_SDWE_R16	268	PE30	E_MDIO
ISI_D18	PE15	201	PE15/KBDR3/ISI_D11_M3	PE31/E_F100/EBI1_SDA10_T16	286	PE31	E_MDINT

The Base Board should have a 1.0k pull-down to boot from NOR Flash



Boot Mode:
BMS = 0 - boot from NOR Flash
BMS = 1 - start internal boot loader

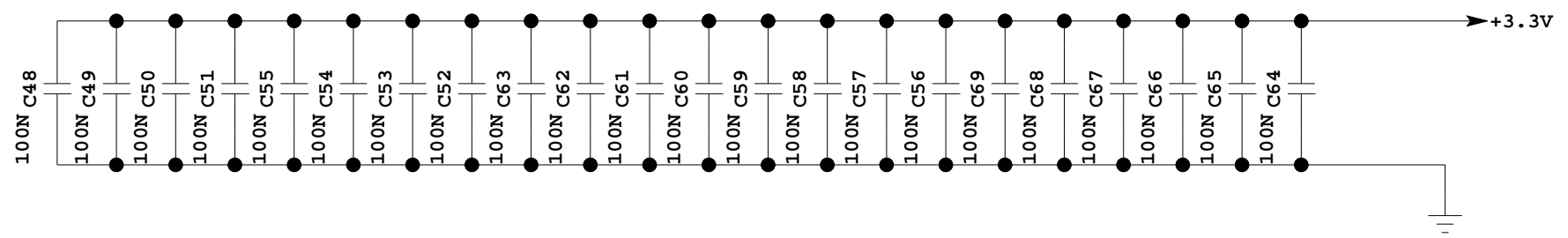
For version coding



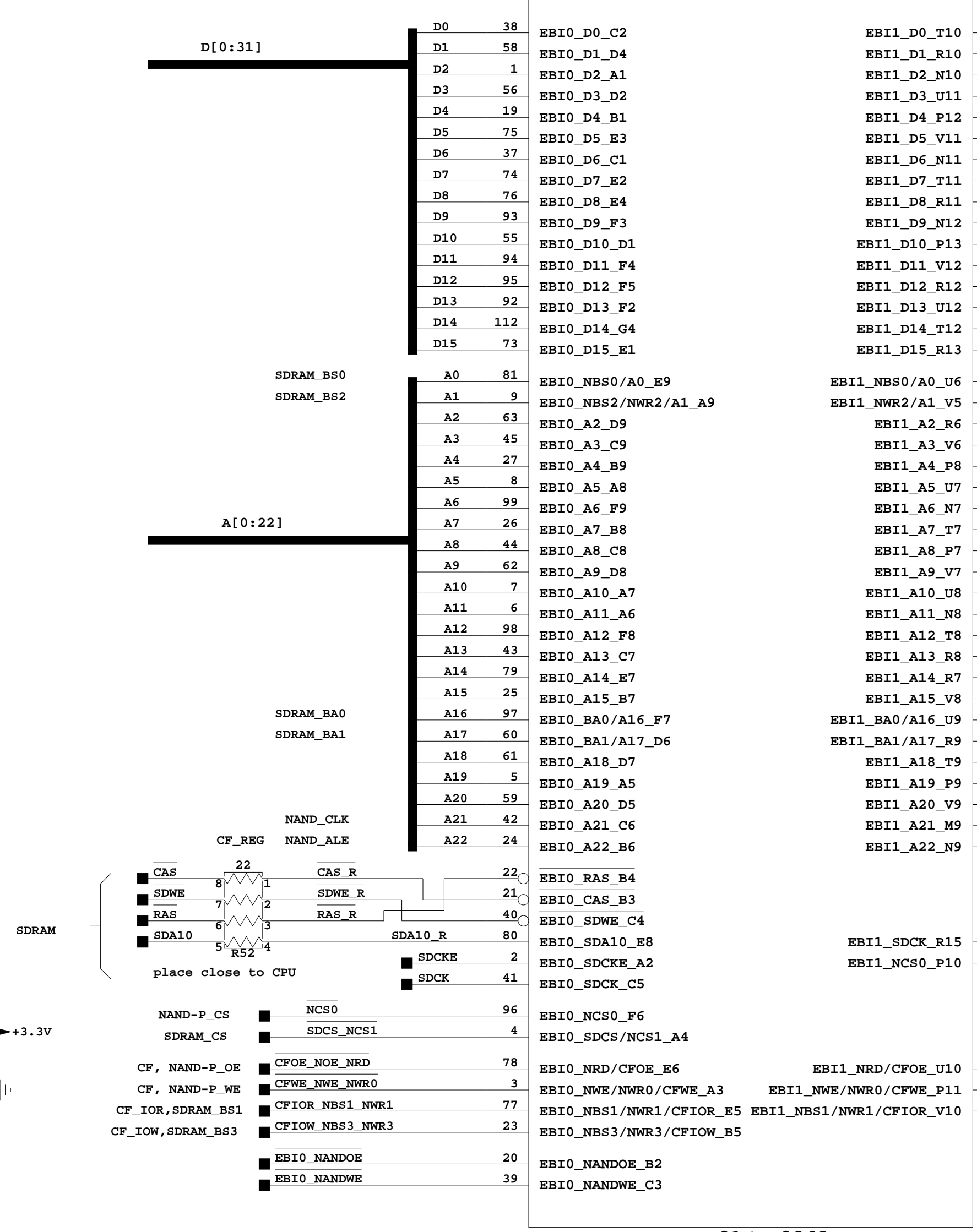
NOT POPULATED

Enable 50MHz oscillator for PHY
1 - enable
0 - disable

AT91SAM9263

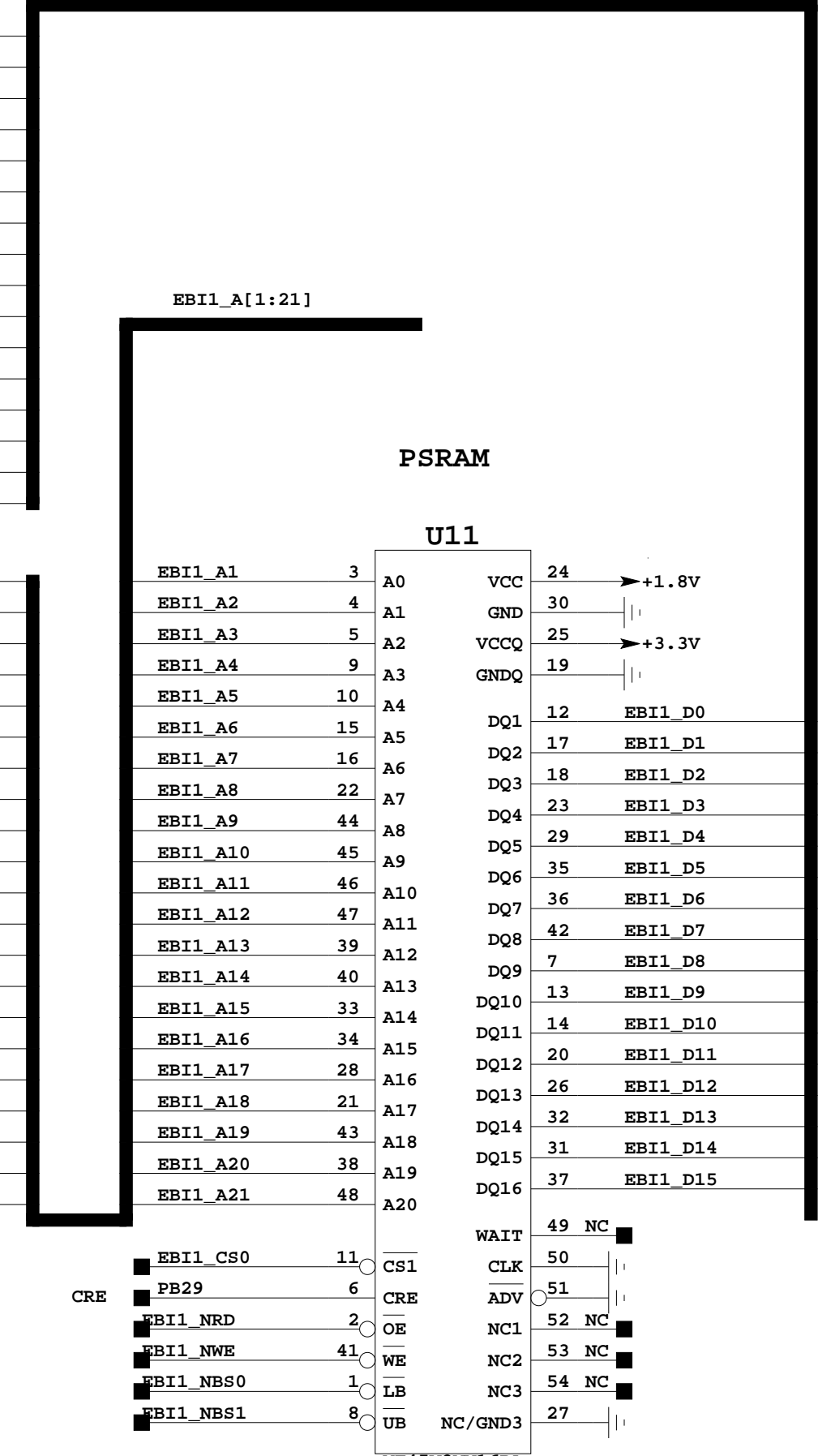


U14

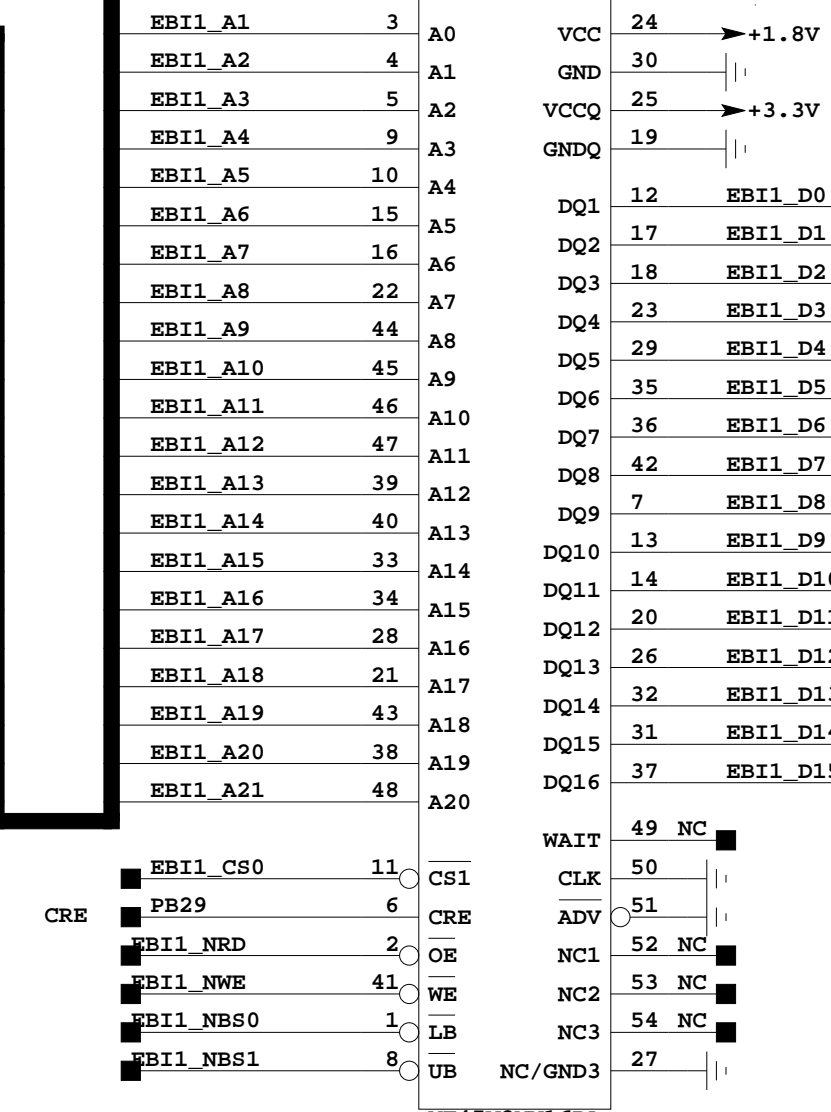


AT91SAM9263

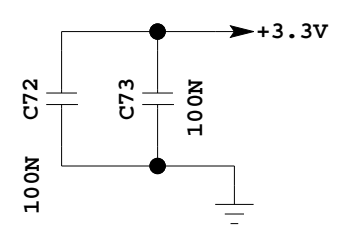
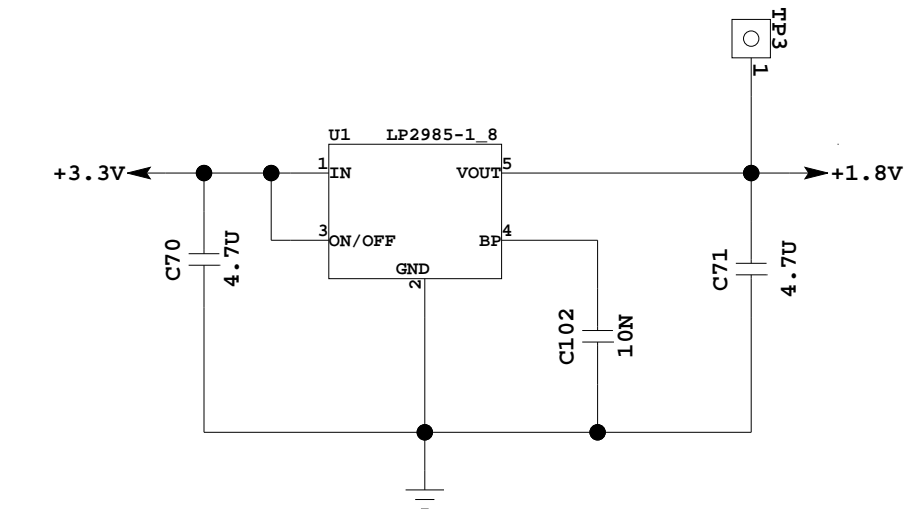
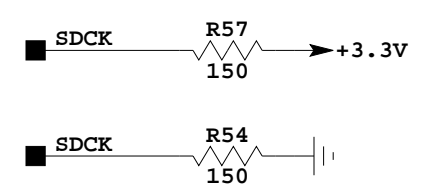
EBI1_D[0:15]

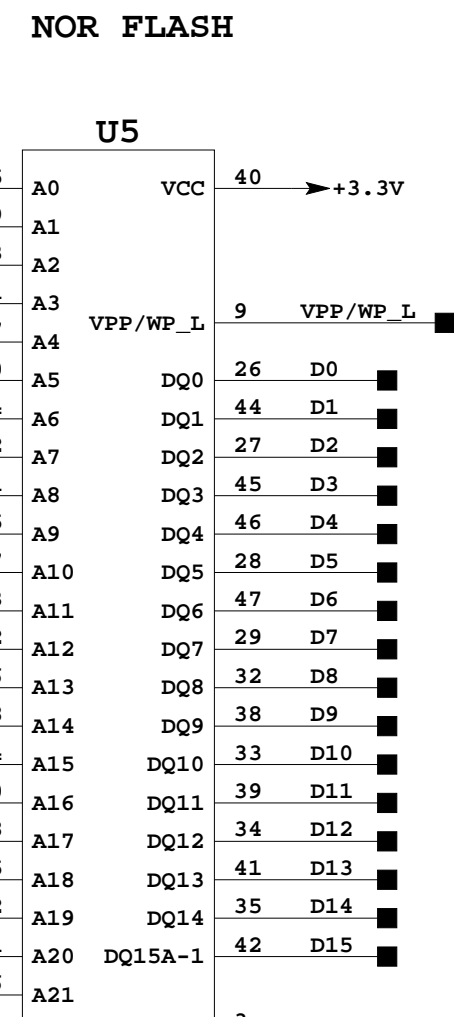
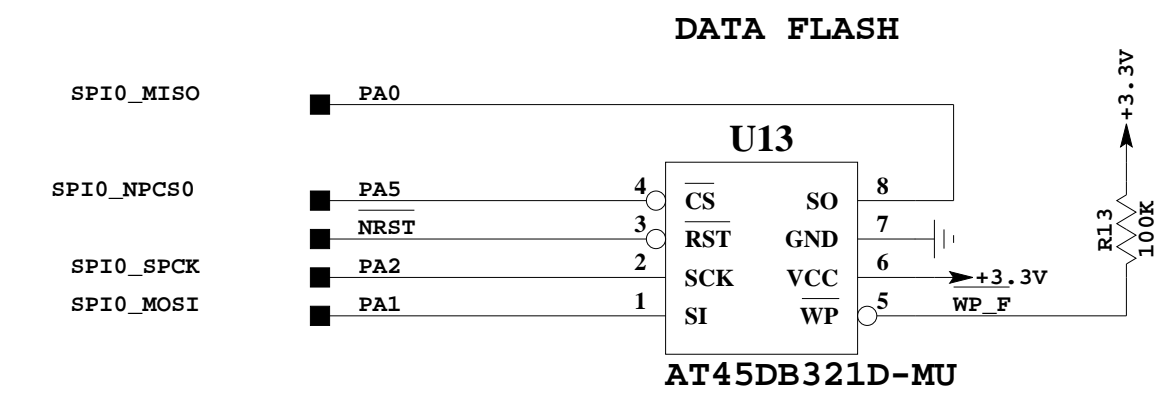
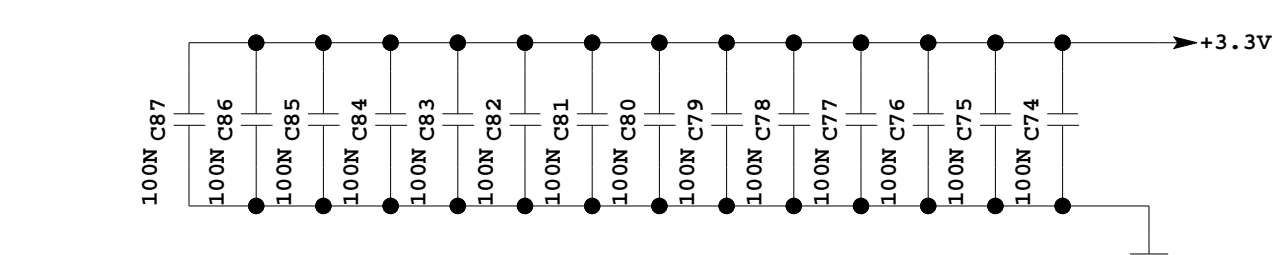
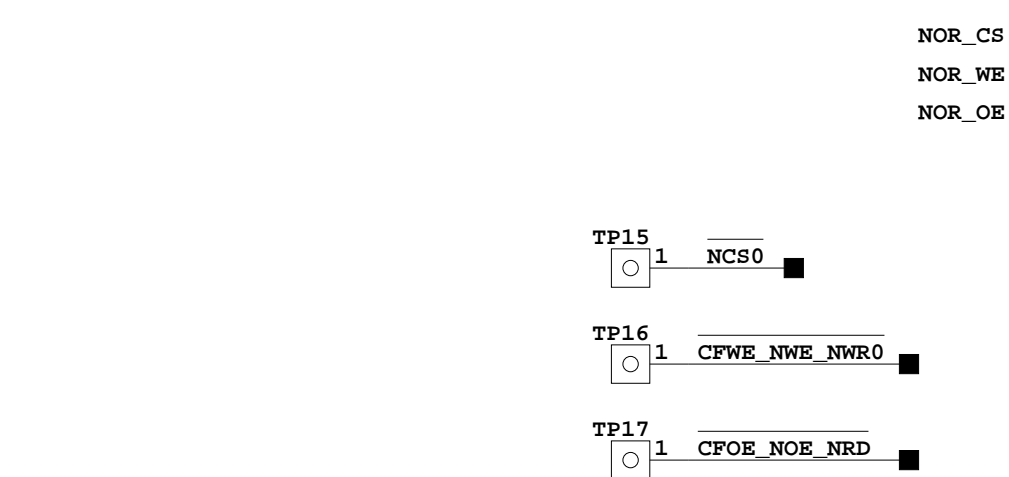
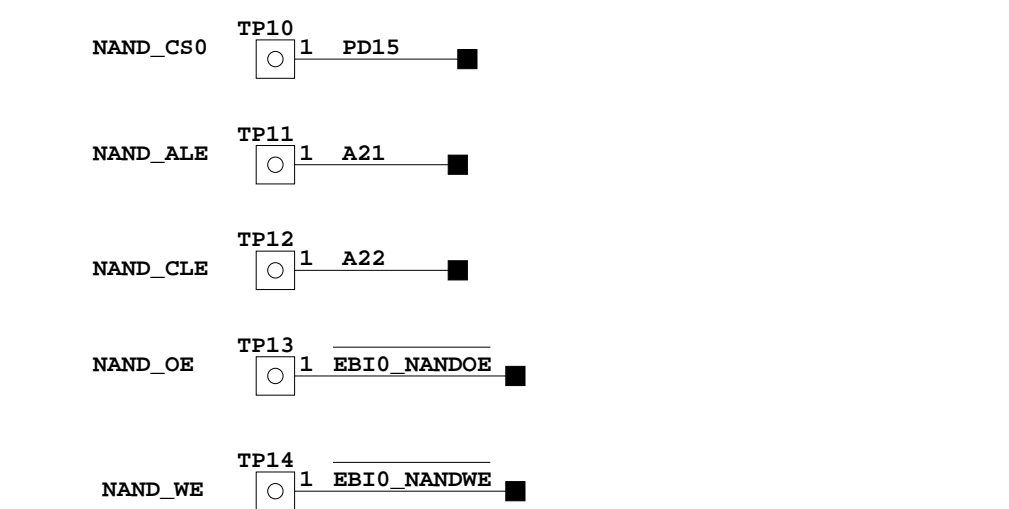
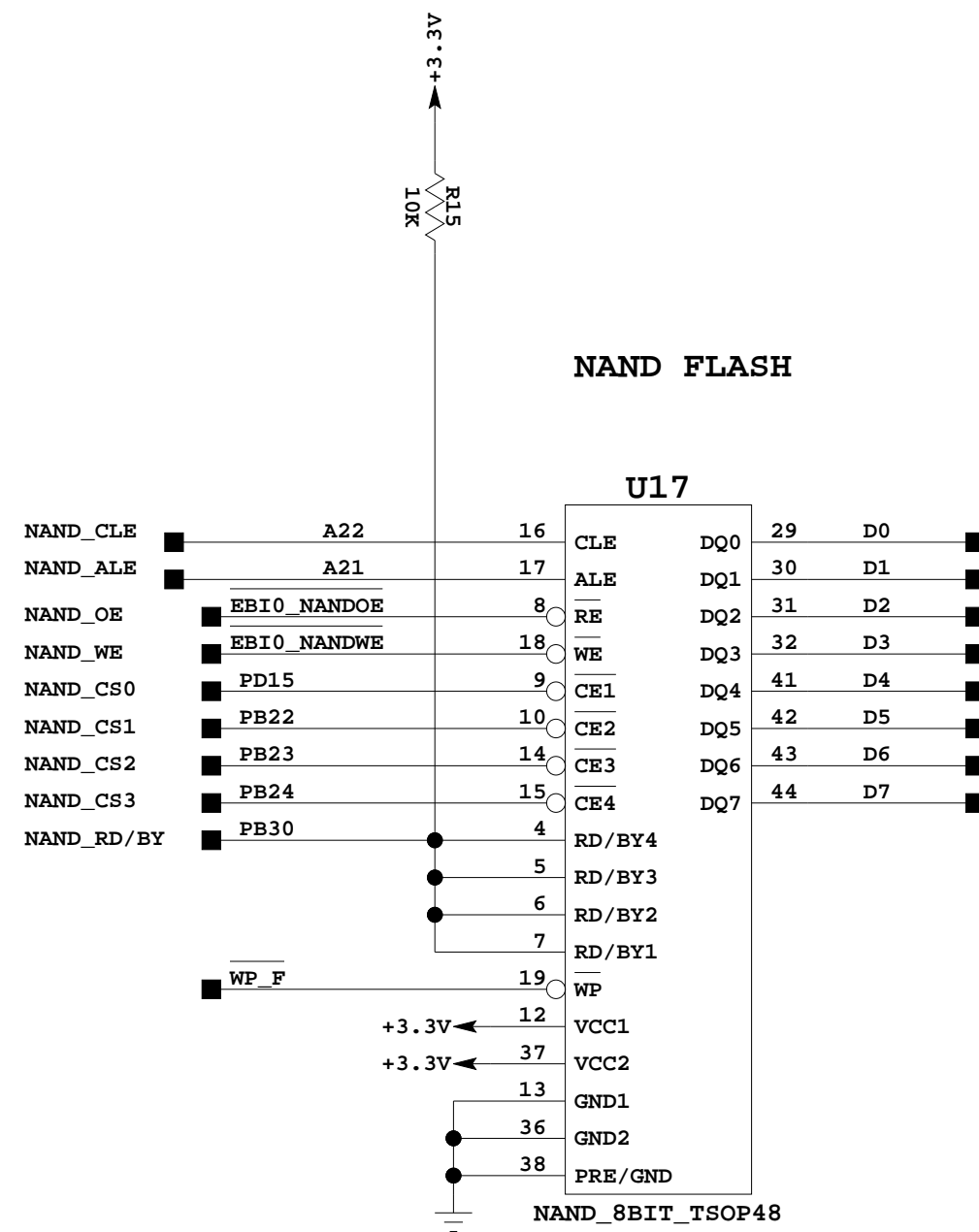
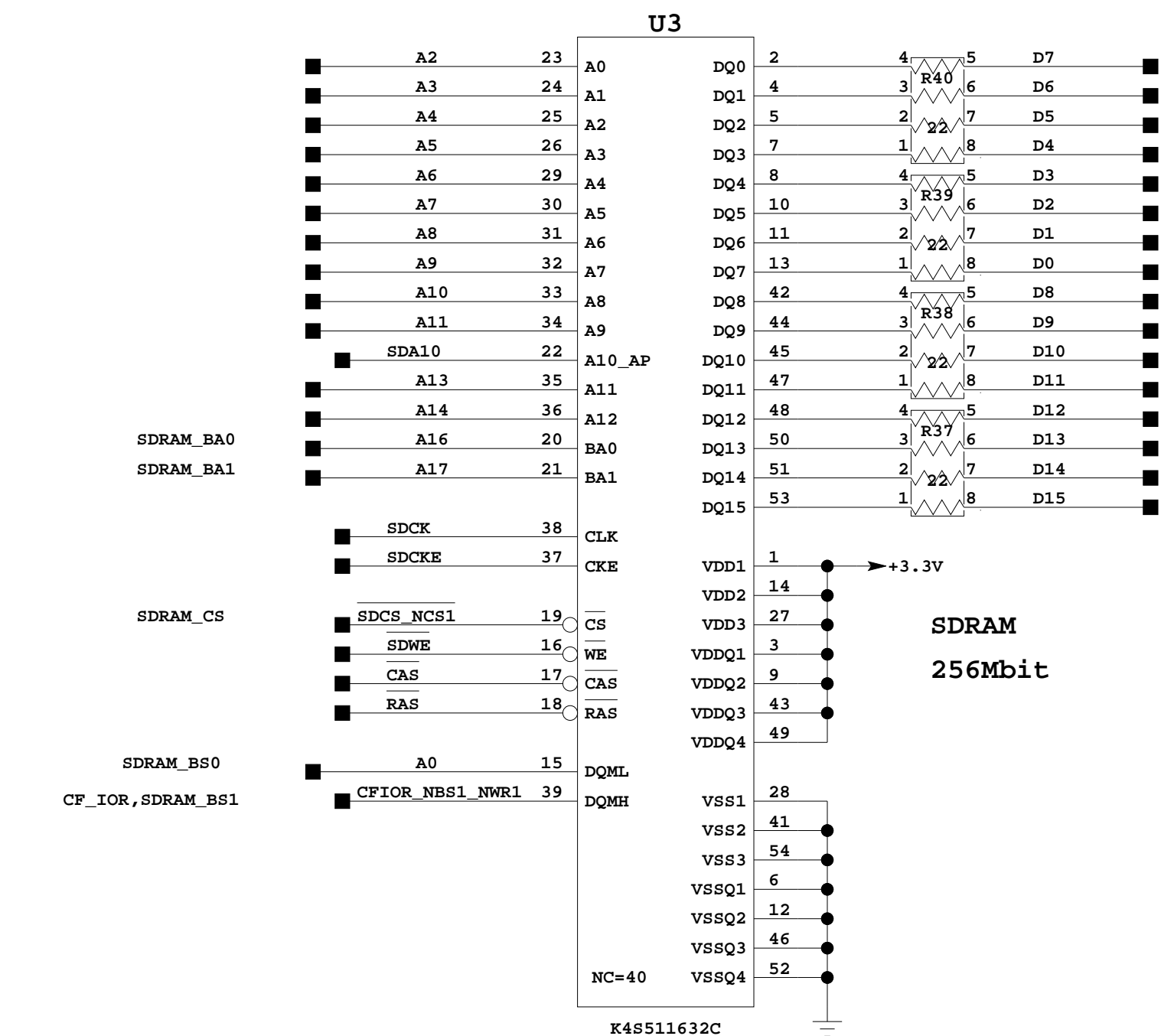
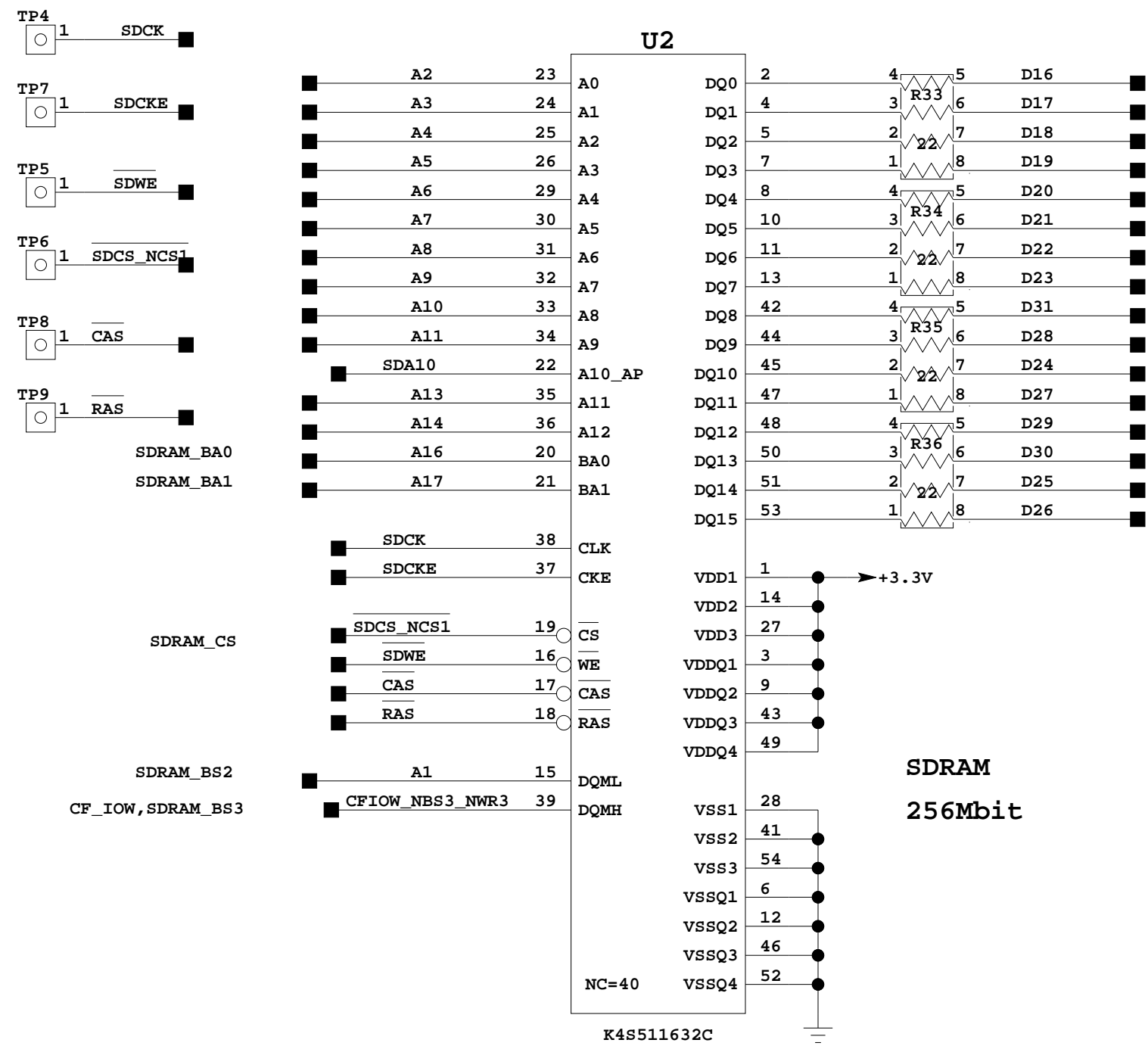


U11

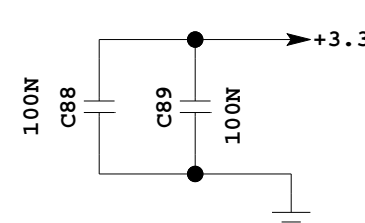


MT45W2M16B (marking code PW420) - CRE must be 0
 MT45W2M16A (marking code PW750) - CRE must be 1

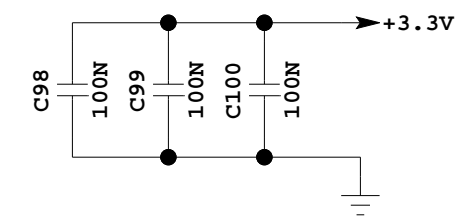
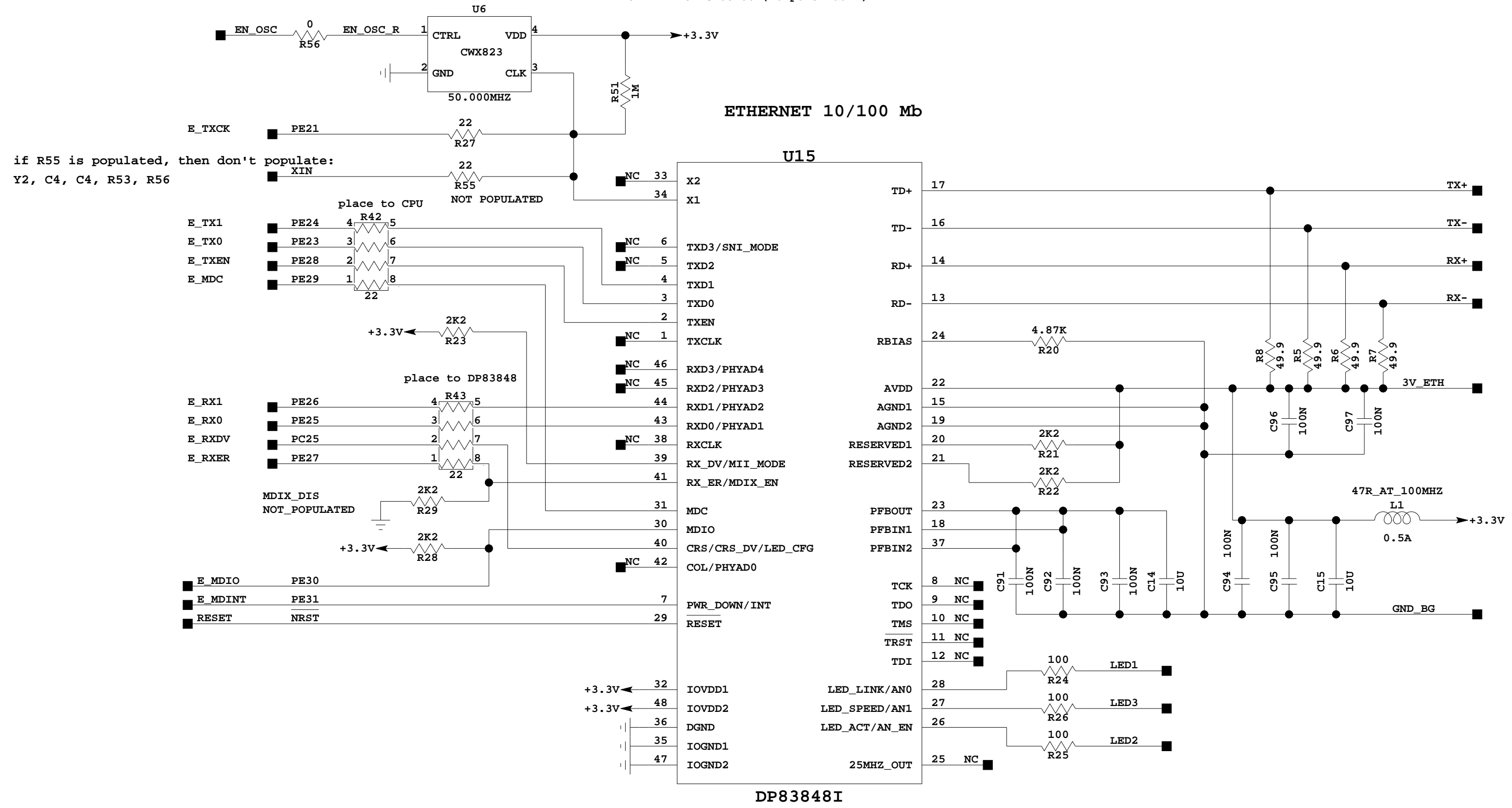




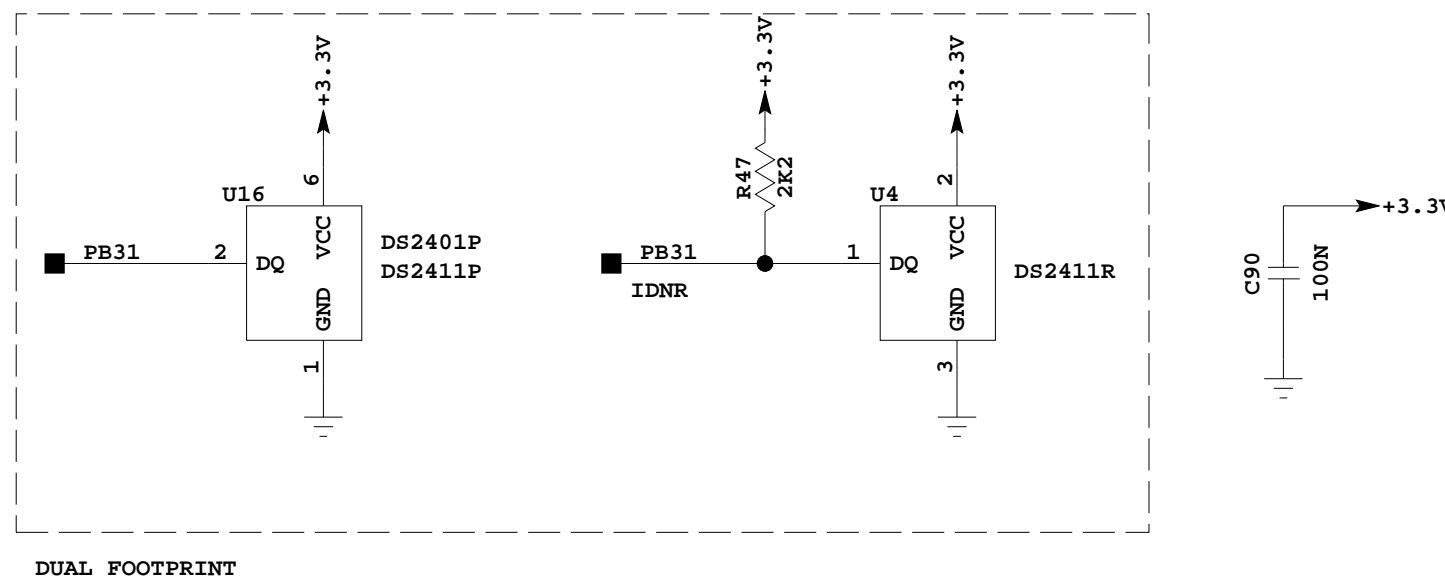
Pin compatible replacements:
 AT49BV322D-70CU
 ES29LV320EB-70WGI
 SST39VF3201B-70-4C-B3E
 M29W320FB-70-ZA
 M29W064FB-70-ZA
 SST38VF6401-90-5C-B3E



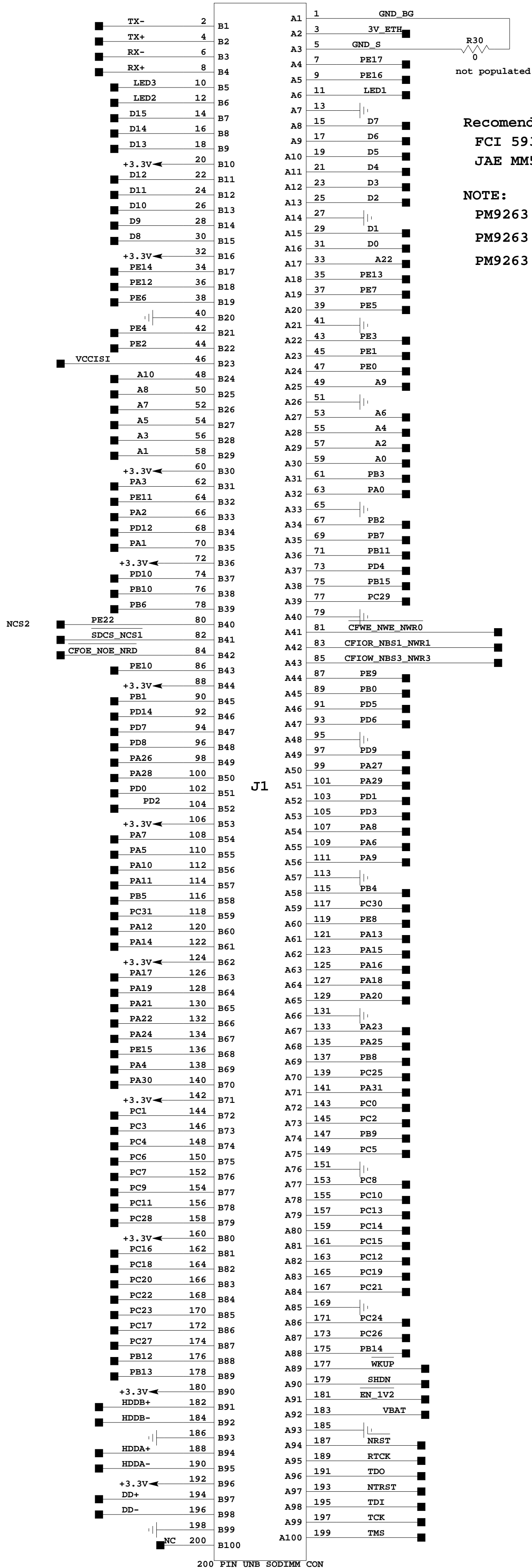
CONNOR-WINFIELD - CWX823-50.0M
 AVX/KVOCERA - K50-3C0SE50.0000MR
 ABRACON - ASV-50.000MHZ-EJ-T
 FOX - FXO-HC735-50 (no power down)



SERIAL NUMBER CHIP



2.5V DDR1 SODIMM200 CONNECTOR

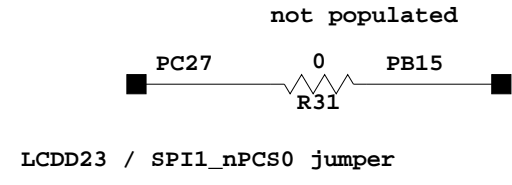


Recommended SODIMM200 2.5V connectors:

- FCI 59354-052FSLF
- JAE MM50-200B1-1E

NOTE:

- PM9263 v1.0 fits in 1.8V and 2.5V SODIMM200 connectors
- PM9263 v1.1 fits only in 2.5V SODIMM200 connectors
- PM9263 v1.2 fits only in 2.5V SODIMM200 connectors



J1

200 PIN UNB SODIMM CON